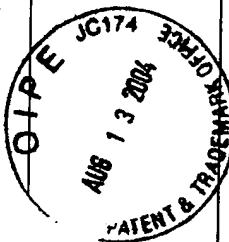




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Applicant: Tadaaki YAMAUCHI, et al. Docket No. 57454-066 Serial/Reg./Patent No. 09/823,996

Title: SEMICONDUCTOR MEMORY DEVICE INTERNALLY PROVIDED WITH LOGIC CIRCUIT WHICH CAN BE READILY CONTROLLED AND CONTROLLING METHOD THEREOF

Date Sent: 8/13/2004 ☐ Hand Carried ☐ Fax ☐ Electronic ☐ Cert. of Mailing ☐ First Class Mail ☐ Express Mail No.

☐ Transmittal Letter

☐ New Patent App ☐ Utility ☐ Design ☐ Cont. ☐ CIP ☐ Div. ☐ PCT ☐ RCE ☐ Prov

☐ Other: \_\_\_\_\_

☐ \_\_\_\_\_ pages of Specification ☐ Letter submitting \_\_\_\_\_ pages of drawings

☐ \_\_\_\_\_ pages of Claims ☐ Req. for Approval of Drawing Amendments

☐ \_\_\_\_\_ pages of Abstract ☐ Req. for Oral Hearing

☐ \_\_\_\_\_ pages of Formal/Informal Drawings ☐ Not. of Appeal ☐ Appeal Brief ☐ Reply Brief

☐ Small Entity ☐ Large Entity ☐ Rule 312 Amendment/Letter

☐ Declaration/Power of Attorney ☒ Req. for Acknowledgement of Cited Art

☐ Recordation of Assignment/Security Agreement ☐ Issue Fee

☐ Information Disclosure Statement ☐ Publication Fee

☐ Form PTO 1449 ☐ Req. for Certificate of Correction

☐ \_\_\_\_\_ copies of cited references ☐ Maintenance Fee for \_\_\_\_\_ years after grant

☐ Preliminary Amendment ☐ Fee Address Indication Form

☐ Response to Missing Parts Notice ☐ Terminal Disclaimer

☐ Resp. to Notice to Correct App. Papers ☐ Petition to Commissioner

☐ Certified Copy of Priority Doc. ☐ Status Inquiry

☐ Claim for Convention Priority ☐ Other \_\_\_\_\_

☐ Response/Amendment to Office Action of \_\_\_\_\_

☐ Request for \_\_\_\_\_ month Extension of Time

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CMS Descrip.: _____								
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Docket No.: 57454-066

SEP 16 2004

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277  
Tadaaki YAMAUCHI, et al. : Confirmation Number: 6448  
Serial No.: 09/823,996 : Group Art Unit: 2188  
Filed: April 03, 2001 : Allowed: August 09, 2004  
Examiner: Jasmine Song

For: SEMICONDUCTOR MEMORY DEVICE INTERNALLY PROVIDED WITH LOGIC  
CIRCUIT WHICH CAN BE READILY CONTROLLED AND CONTROLLING  
METHOD THEREOF

REQUEST FOR ACKNOWLEDGEMENT OF THE CITED ART

Mail Stop ISSUE FEE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

COPY

Sir:

We are in receipt of the Notice of Allowance dated August 09, 2004.

In reviewing the file, it is noted that we have not received acknowledgement of the Information Disclosure Statement and cited art filed January 8, 2004. Enclosed are copies of the Information Disclosure Statement, PTO-1449 and stamped return postcard acknowledgement.

Applicants hereby respectfully request that:

- (1) the appropriately acknowledged PTO-1449 be furnished to Applicants; and
- (2) the record be clarified to confirm that the cited art has been considered and made of record.

Respectfully submitted,

MCDERMOTT WILL & EMERY LLP

  
Stephen A. Becker

Registration No. 26,527

600 13<sup>th</sup> Street, N.W.  
Washington, DC 20005-3096  
202.756.8000 SAB:cg  
Facsimile: 202.756.8087  
Date: August 13, 2004

SEP 16 2004

Applicant: Tadaaki YAMAUCHI, et al.

Docket No. 57454-066

Title: SEMICONDUCTOR MEMORY DEVICE INTERNALLY PROVIDED WITH LOGIC CIRCUIT WHICH CAN BE READILY CONTROLLED AND CONTROLLING METHOD THEREOF

Serial/Reg./Patent No. 09/823,996

Date Sent: 1/8/2004

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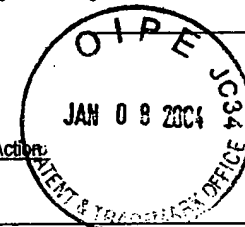
☐ Terminal Disclaimer

☐ Petition to Commissioner

☐ Status Inquiry

☒ Other Translation of Office Action

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